

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.: 7,477,467  
Issue Date: January 13, 2009  
Inventor(s): Pantas Sutardja  
Application No.: 10/722,079  
Filing Date: November 25, 2003  
Title: PREAMP CIRCUIT INCLUDING A LOOPBACK MODE FOR DATA STORAGE  
Attorney Docket: MP0395

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Commissioner for Patents  
Certificate of Correction Branch  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

REQUEST FOR CERTIFICATE OF CORRECTION

Sir:

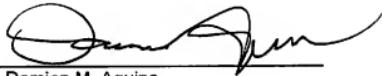
We have reviewed the above referenced patent and have found errors which appear to require a Certificate of Correction.

Attached herewith is an original copy of the Patent and Trademark Office Certificate of Correction form which we request be approved for the above-indicated patent.

Please charge any underpayments or credit any over payment to Deposit Account No. 08-0750.

Respectfully submitted,

Dated: January 29, 2009  
HARNESS, DICKEY & PIERCE, P.L.C.  
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# UNITED STATES PATENT AND TRADEMARK OFFICE

## CERTIFICATE OF CORRECTION

PATENT NO : 7,477,467

APPLICATION NO: 10/722,079

DATED : November 25, 2003

INVENTOR(S) : Pantas Sutardja

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 1, Line 14	Insert -- with -- after "associated"
Column 7, Line 3	Insert -- data -- before "storage"
Column 10, Line 2	Insert -- of -- after "count"
Column 10, Line 37	Insert -- of -- after "count"

MAILING ADDRESS OF SENDER: Harness, Dickey & Pierce, P.L.C.  
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PATENT NO. 7,477,467

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